| L Number | Hits | Search Text | ī DB | Time stamp |
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| | 705385 | interconnect\$3 | USPAT; | 2003/12/15 13:45 |
| [| , 03303 | , incoreomice cy y | US-PGPUB; | 12003/12/13 13:43 |
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| ' | | | EPO; JPO; | |
| 1 | | | DERWENT; | |
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| 2 | 12033 | design\$3 adj3 integrated adj circuit | USPAT; | 2003/12/15 13:53 |
| | | | US-PGPUB; | |
| 1 | | 1 | EPO; JPO; | |
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| : | | | 'IBM_TDB | • |
| 3 | 6 7 7 | specify\$3 same interconnect\$3 same | USPAT; | 2003/12/15 13:54 |
| | | (component or block) | US-PGPUB; | |
| i | | | EPO; JPO; | 1 |
| 1 | | | DERWENT; | |
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| 6 | 2 | optimiz\$5 same design\$3 same (integrated adj circuit) same bandwidth same timing same | USPAT; | 2003/12/15 13:57 |
| | | | US-PGPUB; | ,, |
| | | power | EPO; JPO; | I |
| ' | | | DERWENT; | 1 |
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| , 5 | 5 | optimiz\$5 same design\$3 same (integrated adj | USPAT; | 2003/12/15 13:58 |
| - | 3 | circuit) same bandwidth same timing | US-PGPUB; | 2003/12/13 13.30 |
| 1 | | CITCUITY BUILD BUILDWITCH BUILD ETHING | EPO; JPO; | |
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| 4 | 4.3 | | 'IBM_TDB | 2002/12/15 14 02 |
| ! ** | 42 | optimiz\$5 same design\$3 same (integrated adj | USPAT; | 2003/12/15 14:03 |
| 1 | | circuit) same bandwidth | US-PGPUB; | |
| | | | EPO; JPO; | 1 |
| ; | | | DERWENT; | } |
| ' _ | | 1 252 | IBM_TDB | |
| . 7 | 0 | self?programmable adj4 integrated adj | USPAT; | 2003/12/15 14:05 |
| 1 | | circuit | US-PGPUB; | |
| 1 | | | EPO; JPO; | 1 |
| | | | DERWENT; | ı l |
| , _ | | | IBM_TDB | 1 |
| 8 | 101 | (design\$3 adj3 integrated adj circuit) and | USPAT; | 2003/12/15 14:06 |
| 1 | | (specify\$3 same interconnect\$3 same | US-PGPUB; | ' j |
| | | (component or block)) | EPO; JPO; | ! |
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| _i 9 | 101 | interconnect\$3 and ((design\$3 adj3 | USPAT; | 2003/12/15 14:06 |
| , | j | integrated adj circuit) and (specify\$3 same | US-PGPUB; | , |
| | | <pre>interconnect\$3 same (component or block)))</pre> | EPO; JPO; | ! [|
| | | | DERWENT; | |
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| 10 | 2 | optimiz\$5 same design\$3 same (integrated | USPAT; | 2003/12/15 14:06 |
| ; | - | adj circuit) same bandwidth) and | US-PGPUB; | 1 =,, 25 21.00 |
| ; | | (interconnect\$3 and ((design\$3 adj3 | EPO; JPO; | 1 |
| ! | | integrated adj circuit) and (specify\$3 same | DERWENT; | : |
| i | | interconnect\$3 same (component or block))) | IBM TDB | |
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